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Substitute for form 1449A/PTO			•	Complete if Known			
		٠		Application Number	09/608,624		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT			CLOSURE	Filing Date	June 30, 2000		
			PPLICANT	First Named Inventor	Stephan J. JOURDAN et al.	et al.	
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(Use as many sheets as necessary)				Examiner Name	Henry Tsai /	, 	
Sheet	1	of	1	Attorney Docket Number	02207/8609		

			U.S. PATENT D	OCUMENTS	V.
Fireminas	carriner Cite	Document Number	Publication Date	Name of Palentee or Applicant of	Pages, Columns, Lines, Where Relevant
Initials *		Number - Kind Code ² (if known)	1 101 00 1000/	Cited Document	Passages or Relevant Figures Appear
		US- 6,535,959	03-18-03	Ramprasad et al.	
		US- 6,427,188	07-30-02	Lyon et al.	7
		US- 5,924,092	07-1999	Johnson	
		US- 5,913,223	06-15-99	Sheppard et al.	
12		US Publication No. 2002/0078327 A1	06-20-02	Jourdan et al.	
		1	-		

	FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
		Country Code ³ – Number ⁴ - Kind Code ⁵ (<i>if known</i>)				English Abstract	
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		NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			
45		Bellas et al., "Architectural and Compiler Techniques for Energy Reduction in High Performance Microprocessors," <i>IEEE Transactions on VLSI</i> , Vol. 8, No. 3, June 2000, pp. 317-326.			
	Glaskowsky, Peter N., "Pentium 4 (Partially) Previewed," Microprocessor Report, Vol. 14, Archive 8, Augu 2000, p. 1, pp. 11-13.				
		Manne et al., "Pipeline Gating: Speculation Control for Energy Reduction," ACM SIGARCH, Proceedings of the 25th Annual International Symposium on Computer Architecture, IEEE Computer Society Tech. Comm. on Computer Architecture, June 27-July 1, 1998, Barcelona, Spain, pp. 132-141.			
		Papworth, David B., "Tuning the Pentium Pro Microarchitecture," <i>IEEE Micro</i> , IEEE Computer Society, Vol. 16, No. 2, April 1996, pp.8-15.			
		Solomon et al., "Micro-Operation Cache: A Power Aware Frontend for Variable Instruction Length ISA," ISLPED '01, August 6-7, 2001, Huntington Beach, CA, pp. 4-9.			
25		Upton, Michael, "The Intel Pentium® 4 Processor," http://www.intel.com/pentium4, October 2000, 15 pp.			
Examiner Signature		Jeny Jun Date Considered 12/23/64			

iffi by the public which is its Re (and by the UEPTO to precise) an application. Confidentially is governed by 35 U.S.C. 122 and 37 CFR 1.14. obtation from it the USPTO Time will say depending upon the individual case. Any comments on the amount of time you request to complete delimate Office, U.S. Oxperiment of Commerce, P.O. 80s 1150, Assertatic, VX 22313-1490, DO NOT SERV PEES OF CONTRETEED FORMS